



**PRODUCT NAME :** Q4008 8A 400V TRIAC

**PRICE :** Rs 35.00

**SKU :** RM1997

**DESCRIPTION**

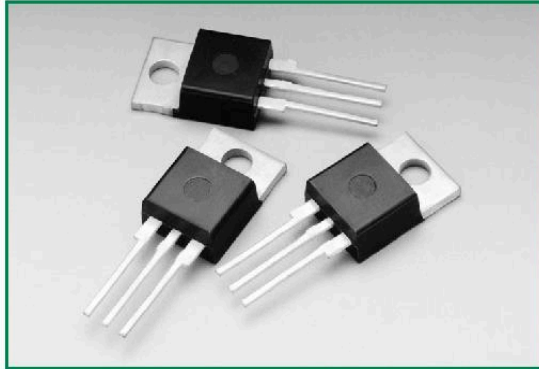


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**Teccor® brand Thyristors**  
 4 / 6 / 8 / 10 / 15 Amp Quadracs



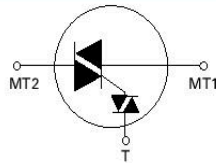
**RoHS QxxxxLTx Series**



**Agency Approval**

Agency	Agency File Number
	L Package : E71639

**Schematic Symbol**



**Main Features**

Symbol	Value	Unit
$I_{T(RMS)}$	4 to 15	A
$V_{DRM} / V_{RRM}$	400 to 600	V
DIAC $V_{BO}$	33 to 43	V

**Description**

The Quadrac is an internally triggered Triac designed for AC switching and phase control applications. It is a Triac and DIAC in a single package, which saves user expense by eliminating the need for separate Triac and DIAC components.

Standard type devices normally operate in Quadrants I & III triggered from AC line.

Alternistor type Quadracs are used in circuits requiring high dv/dt capability.

**Features & Benefits**

- RoHS Compliant
- Glass – passivated junctions
- Voltage capability up to 600 V
- Surge capability up to 200 A

**Applications**

Excellent for AC switching and phase control applications such as lighting and heating. Typical applications are AC solid-state switches, light dimmers, power tools, home/ brown goods and white goods appliances.

Alternistor Quadracs (no snubber required) are used in applications with extremely inductive loads requiring highest commutation performance.

Internally constructed isolated package is offered for ease of heat sinking with highest isolation voltage.

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**Absolute Maximum Ratings**

Symbol	Parameter	Value					Unit	
		Qxx04LT	Qxx06LT / Qxx06LTH	Qxx08LT / Qxx08LTH	Qxx10LT / Qxx10LTH	Qxx15LT / Qxx15LTH		
$I_{T(RMS)}$	RMS forward current	Qxx04LT: $T_c = 95^\circ\text{C}$ Qxx06LT/Qxx08LT/Qxx10LT: $T_c = 90^\circ\text{C}$ Qxx15LT: $T_c = 80^\circ\text{C}$					A	
$I_{TSM}$	Peak non-repetitive surge current	single half cycle; $f = 50\text{Hz}$ ; $T_J$ (initial) = $25^\circ\text{C}$	46	65	83	100	167	A
		single half cycle; $f = 60\text{Hz}$ ; $T_J$ (initial) = $25^\circ\text{C}$	55	80	100	120	200	
$I^2t$	$I^2t$ value for fusing	$t_p = 8.3\text{ms}$					$\text{A}^2\text{s}$	
$di/dt$	Critical rate-of-rise of on-state current	$f = 60\text{Hz}; T_J = 125^\circ\text{C}$		70		100	$\text{A}/\mu\text{s}$	
$I_{GM}$	Peak gate current	$T_J = 125^\circ\text{C}$					1.5	A
$T_{stg}$	Storage temperature range						-40 to 150	$^\circ\text{C}$
$T_J$	Operating junction temperature range						-40 to 125	$^\circ\text{C}$

Note: xx = voltage

**Electrical Characteristics ( $T_J = 25^\circ\text{C}$ , unless otherwise specified) – Standard Quadrac**

Symbol	Test Conditions		Value					Unit	
			Qxx04LT	Qxx06LT	Qxx08LT	Qxx10LT	Qxx15LT		
$I_H$	$I_T = 200\text{mA}$ (initial)	MAX.	40	50	60	60	70	mA	
$dv/dt$	$V_D = V_{DRM}$ ; gate open; $T_J = 100^\circ\text{C}$	MIN.	400V	75	150	175	200	300	$\text{V}/\mu\text{s}$
			600V	50	125	150	175	200	
	$V_D = V_{DRM}$ ; gate open; $T_J = 125^\circ\text{C}$	MIN.	400V	50	100	120	150	200	
			600V	50	85	100	120	150	
$dv/dt(c)$	$di/dt(c) = 0.54 \times I_{T(RMS)} / \text{ms}; T_J = 125^\circ\text{C}$	MIN.	3	4				$\text{V}/\mu\text{s}$	
$t_{gr}$	(note 1)	TYP.	3				$\mu\text{s}$		

(1) Reference test circuit in figure 10 and waveform in figure 11;  $C_T = 0.1\mu\text{F}$  with  $0.1\mu\text{s}$  rise time.  
 Note: xx = voltage

**Electrical Characteristics ( $T_J = 25^\circ\text{C}$ , unless otherwise specified) – Alternistor Quadrac**

Symbol	Test Conditions		Value				Unit	
			Qxx06LTH	Qxx08LTH	Qxx10LTH	Qxx15LTH		
$I_H$	$I_T = 20\text{mA}$ (initial)	MAX.	50	50	60	70	mA	
$dv/dt$	$V_D = V_{DRM}$ ; gate open; $T_J = 100^\circ\text{C}$	MIN.	400V	575		925		$\text{V}/\mu\text{s}$
			600V	425		775		
	$V_D = V_{DRM}$ ; gate open; $T_J = 125^\circ\text{C}$	MIN.	400V	450		700		
			600V	350		600		
$dv/dt(c)$	$di/dt(c) = 0.54 \times I_{T(RMS)} / \text{ms}; T_J = 125^\circ\text{C}$	MIN.	25		30		$\text{V}/\mu\text{s}$	
$t_{gr}$	(note 1)	TYP.	3				$\mu\text{s}$	

(1) Reference test circuit in figure 10 and waveform in figure 11;  $C_T = 0.1\mu\text{F}$  with  $0.1\mu\text{s}$  rise time.  
 Note: xx = voltage

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**Trigger DIAC Specifications**

Symbol	Test Conditions		Value	Unit
$\Delta V_{BO}$	Breakover Voltage Symmetry	MAX.	3	V
$V_{BO}$	Breakover Voltage, forward and reverse	MIN.	33	V
		MAX.	43	
$[\Delta V_{\pm}]$	Dynamic Breakback Voltage, forward and reverse (note 1)	MIN.	5	V
$I_{BO}$	Peak Breakover Current	MAX.	25	$\mu$ A
$C_T$	Trigger Firing Capacitance	MAX.	0.1	$\mu$ F

(1) Reference test circuit in figure 10 and waveform in figure 11.

**Static Characteristics**

Symbol	Test Conditions		Value	Unit
$V_{TM}$	$I_T = 1.41 \times I_{T(rms)}$ ; $A_i$ ; $t_p = 380\mu s$	MAX.	1.6	V
$I_{DRM} / I_{RRM}$	$V_{DRM} / V_{RRM}$	$T_J = 25^\circ C$	10	$\mu$ A
		$T_J = 100^\circ C$	500	
		$T_J = 125^\circ C$	2000	

**Thermal Resistances**

Symbol	Parameter		Value	Unit
$R_{\theta(J-C)}$	Junction to case (AC)	Qxx04LT	3.6	$^\circ C/W$
		Qxx06LT / Qxx06LTH	3.3	
		Qxx08LT / Qxx08LTH	2.8	
		Qxx10LT / Qxx10LTH	2.6	
		Qxx15LT / Qxx15LTH	2.1	
$R_{\theta(J-A)}$	Junction to ambient		50	$^\circ C/W$

Note : xx = voltage

