PRODUCT NAME : 2N5038 NPN Power Transistor

PRICE : Rs 20.00
SKU : RM2072

DESCRIPTION

Features

- Collector-Emitter Volt (Vceo): 150V
- Collector-Base Volt (Vcbo): 150V
- Collector Current (Ic): 20.0A
- hfe: 20-100 @ 12.0A
- Power Dissipation (Ptot): 140W
- Type: NPN

MOTOROLA
SEMICONDUCTOR TECHNICAL DATA

Order this document
by 2N5038/D

2N5038*
2N5039

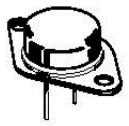
*Motorola Preferred Device

NPN Silicon Transistors

. . . fast switching speeds and high current capacity ideally suit these parts for use in switching regulators, inverters, wide–band amplifiers and power oscillators in industrial and commercial applications.

- High Speed — t_f = 0.5 µs (Max)
- High Current — $I_{C(max)}$ = 30 Amps
- Low Saturation — $V_{CE(sat)}$ = 2.5 V (Max) @ I_C = 20 Amps

20 AMPERE
NPN SILICON
POWER TRANSISTORS
75 and 90 VOLTS
140 WATTS



CASE 1–07
TO–204AA
(TO–3)

***MAXIMUM RATINGS**

Rating	Symbol	2N5038	2N5039	Unit
Collector–Base Voltage	V_{CBO}	150	120	Vdc
Collector–Emitter Voltage	V_{CEV}	150	120	Vdc
Emitter–Base Voltage	V_{EBO}	7		Vdc
Collector Current — Continuous Peak (1)	I_C I_{CM}	20 30		Adc
Base Current — Continuous	I_B	5		Adc
Total Device Dissipation @ T_C = 25°C Derate above 25°C	P_D	140 0.8		Watts W/°C
Operating and Storage Junction Temperature Range	T_J, T_{stg}	–65 to +200		°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.25	°C/W

* Indicates JEDEC Registered Data.
(1) Pulse Test: Pulse Width ≤ 10 ms, Duty Cycle ≤ 50%.

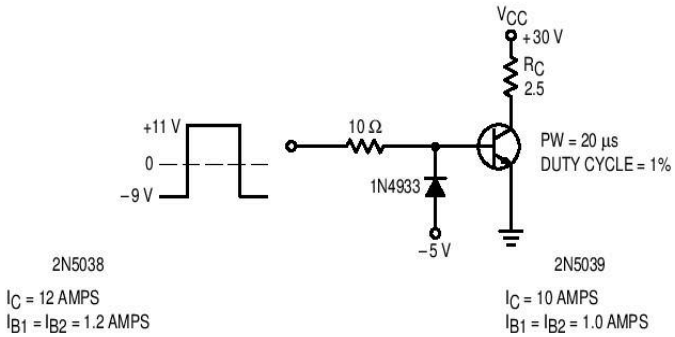


Figure 1. Switching Time Test Circuit

Preferred devices are Motorola recommended choices for future use and best overall value.
REV 7

MOTOROLA

2N5038 2N5039

***ELECTRICAL CHARACTERISTICS** (T_C = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Sustaining Voltage (1)		$V_{CEO(sus)}$			Vdc
(I_C = 200 mAdc, I_B = 0)	2N5038		90	—	
	2N5039		75	—	
Collector Cutoff Current		I_{CEX}			mAdc
(V_{CE} = 140 Vdc, $V_{BE(off)}$ = 1.5 V)	2N5038		—	50	
(V_{CE} = 110 Vdc, $V_{BE(off)}$ = 1.5 V)	2N5039		—	50	
(V_{CE} = 100 Vdc, $V_{BE(off)}$ = 1.5 Vdc, T_C = 150°C)	2N5038		—	10	
(V_{CE} = 85 Vdc, $V_{BE(off)}$ = 1.5 Vdc, T_C = 150°C)	2N5039		—	10	
Emitter Cutoff Current		I_{EBO}			mAdc
(V_{EB} = 5 Vdc, I_C = 0)	2N5038		—	5	
	2N5039		—	15	
(V_{EB} = 7 Vdc, I_C = 0)	Both		—	50	
ON CHARACTERISTICS (1)					
DC Current Gain		h_{FE}			—
(I_C = 12 Adc, V_{CE} = 5 Vdc)	2N5038		20	100	
(I_C = 10 Adc, V_{CE} = 5 Vdc)	2N5039		20	100	
Collector–Emitter Saturation Voltage (I_C = 20 Adc, I_B = 5 Adc)		$V_{CE(sat)}$	—	2.5	Vdc
Base–Emitter Saturation Voltage (I_C = 20 Adc, I_B = 5 Adc)		$V_{BE(sat)}$	—	3.3	Vdc
DYNAMIC CHARACTERISTICS					
Magnitude of Common–Emitter Small–Signal Short–Circuit Forward Current Transfer Ratio (I_C = 2 Adc, V_{CE} = 10 Vdc, f = 5 MHz)		$\|h_{fe}\|$	12	—	—
SWITCHING CHARACTERISTICS					
RESISTIVE LOAD					
Rise Time	(V_{CC} = 30 Vdc)	t_r	—	0.5	µs
Storage Time	(I_C = 12 Adc, I_{B1} = I_{B2} = 1.2 Adc) 2N5038	t_s	—	1.5	µs
Fall Time	(I_C = 10 Adc, I_{B1} = I_{B2} = 1 Adc) 2N5039	t_f	—	0.5	µs

* Indicates JEDEC Registered Data.
(1) Pulse Test: Pulse Width ≤ 300, µs, Duty Cycle ≤ 2%.

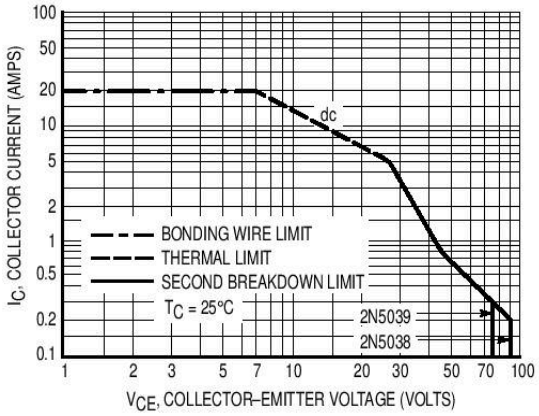


Figure 2. Forward Bias Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

Second breakdown pulse limits are valid for duty cycles to 10%. At high case temperatures, thermal limitations may reduce the power that can be handled to values less than the limitations imposed by second breakdown.